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DOCKET NO.: 2343-182-27

ASSISTANT COMMISSIONER FOR PATENTS
PO BOX 1450
ALEXANDRIA, VA 22313-1450

Re: Serial No.: 10/774,599
Applicant(s): ROBERT B. REESE, ET AL.
Filing Date: FEBRUARY 10, 2004
For: METHOD FOR EARLY EVALUATION IN
MICROPIPELINE PROCESSORS
Group Art Unit: 2124
Examiner:

SIR:

Attached hereto for filing are the following papers:

INFORMATION DISCLOSURE STATEMENT
PTO FORM 1449
CITED DOCUMENTS (5)

Our check in the amount of \$ -0- is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary extension of time to make the filing of the attached documents timely, please charge or credit the difference to Deposit Account No. 50-1442. Further, if these papers are not considered timely filed, then a request is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

PIPER RUDNICK LLP

Steven B. Kelber
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Registration No.: 30,073

Lisa K. Norton
Registration No.: 44,977

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Robert B. REESE, et al.

GROUP ART UNIT: 2124

SERIAL NUMBER: 10/774,599

EXAMINER:

FILED: February 10, 2004

FOR: METHOD FOR EARLY EVALUATION IN MICROPIPELINE PROCESSORS
INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97

Assistant Commissioner for Patents
PO BOX 1450
ALEXANDRIA, VA 22313-1450

Sir:

Applicant(s) wish(es) to disclose the following information.

REFERENCES

- Applicant(s) wish(es) to make of record the documents listed on the attached Form PTO-1449. Copies of the listed documents are attached, where required, as are either statements of relevancy or any readily available full or partial English translations of any non-English-language documents.

RELATED CASES

- Attached is a list of Applicant's(s') pending applications and issued patents which may be related to the present application. Copies of the documents, where required, are attached along with Form PTO-1449.

CERTIFICATION

The undersigned certifies that

- each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign or international patent office in a counterpart foreign or international application for the first time (to the knowledge of the undersigned, having made reasonable inquiry) not more than three months prior to the filing of this statement.
- no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign or international patent office in a counterpart foreign or international application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement.

BASIS FOR CONSIDERATION

This Information Disclosure Statement is filed:

- without fee and within three months of the filing date of the application.
- without fee and within three months of the date of entry of the U.S. national stage.
- without fee and before the mailing date of a first Office Action on the merits (to the knowledge of the undersigned).
- without fee and with the appropriate certification above.
- without fee and with a new CPA application.
- without fee and with a Request for Continued Examination.
- with fee and before the mailing date of any Final Office Action, Notice of Allowance or an action that otherwise closes prosecution (to the knowledge of the undersigned).
- with fee, appropriate certification above, and before payment of the Issue Fee.

DEPOSIT ACCOUNT

- Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 50-1442.

Respectfully submitted,

PIPER RUDNICK LLP

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Form PTO 1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	DOCKET NO.	SERIAL NO.
		2343-182-27	10/774,599
		APPLICANT	
		Robert B. REESE, et al.	
LIST OF REFERENCES CITED BY APPLICANT (Use Several Sheets if Necessary)		FILING DATE	GROUP ART UNIT
		February 10, 2004	2124



U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO
	AK				
	AL				
	AM				

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

	AN	Ivan E. Sutherland, "Micropipelines", Communications of the ACM, Vol. 32, No. 6, June 1989, pp. 720-738.
	AO	D. E. Muller, et al., "A Theory of Asynchronous Circuits", Proc. Int. Symp. On Theory of Switching, Vol. 29, pp. 204-243 (1959).
	AP	M. E. Dean, et al., "Efficient Self-Timing with Level-Encoded 2-Phase Dual-Rail (LEDR)", Advanced Research in VLSI (1991).
	AQ	D. H. Linder, et al., "Phased Logic: Supporting the Synchronous Design Paradigm with Delay Insensitive Circuitry", IEEE Transactions on Computers, Vol. 45, No. 9, September 1996.
	AR	F. Commoner, et al., "Marked Directed Graphs", J. Computer and Systems Sciences, Vol. 5, pp. 511-523 (1971).

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.